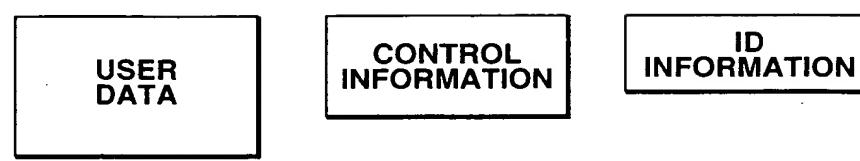
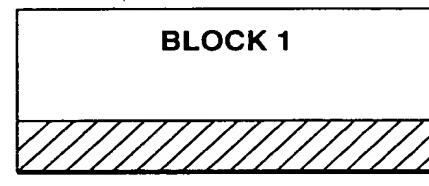


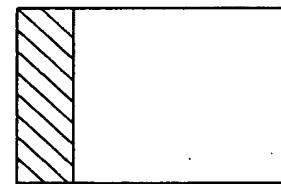
LOGICAL STRUCTURE



ECC STRUCTURE

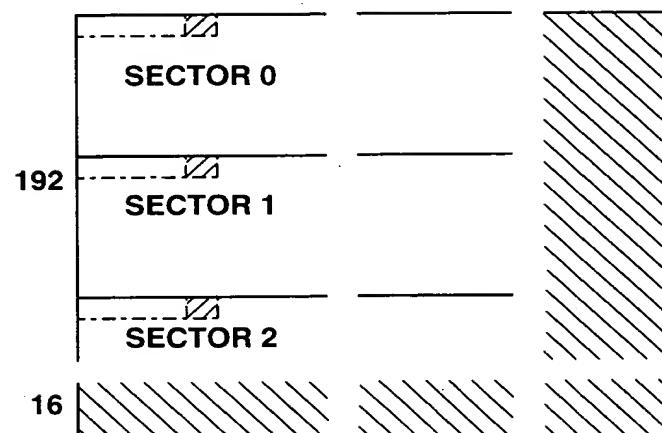


PHYSICAL STRUCTURE



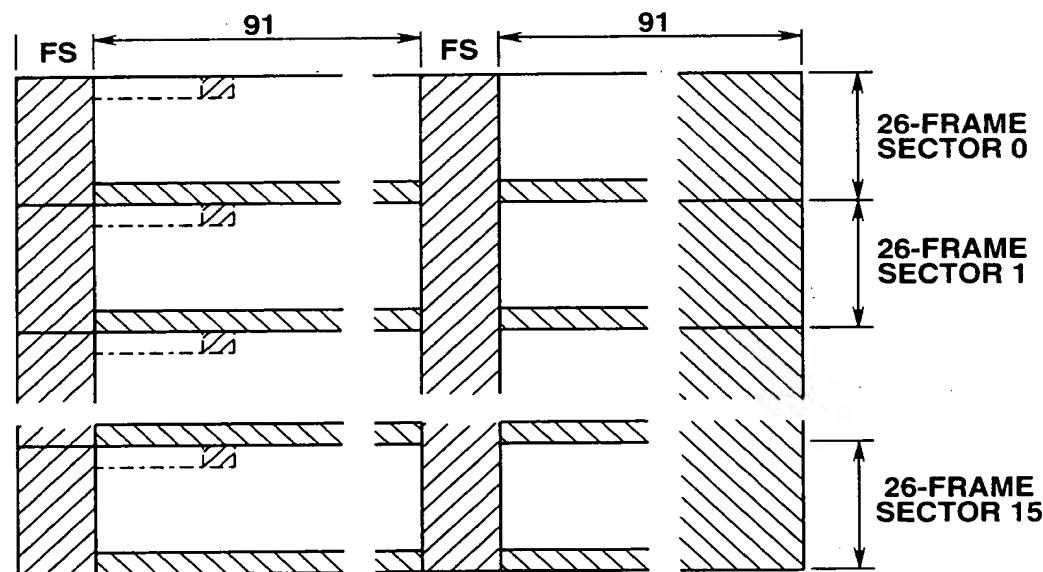
**FIG.1**

ECC BLOCK 182

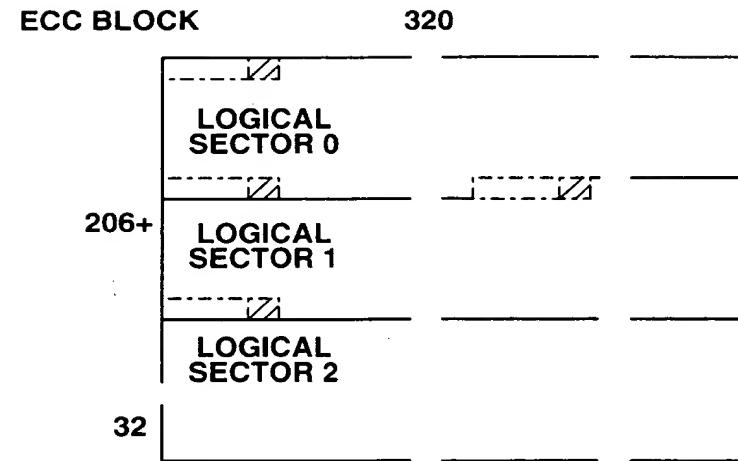


**FIG.2**

PHYSICAL BLOCK

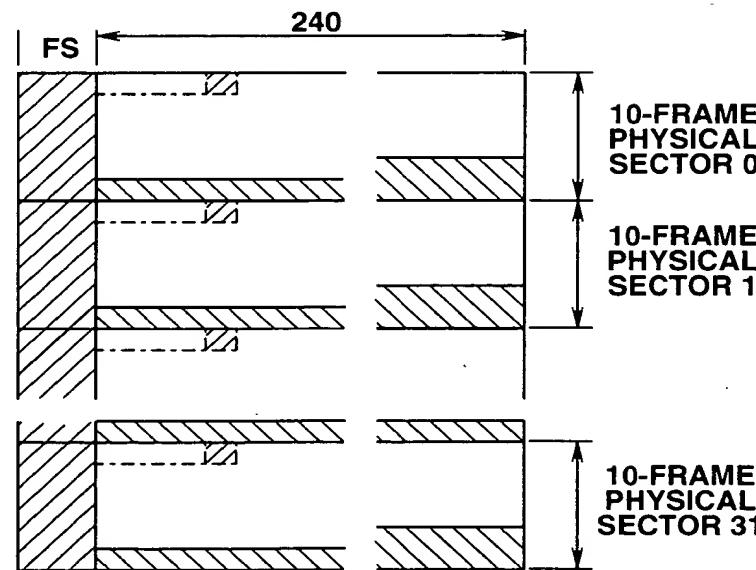


**FIG.3**



**FIG.4**

**PHYSICAL STRUCTURE**

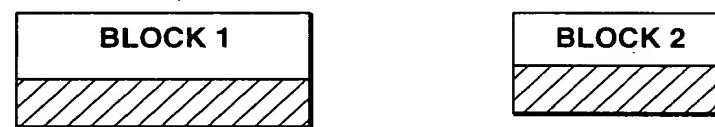


**FIG.5**

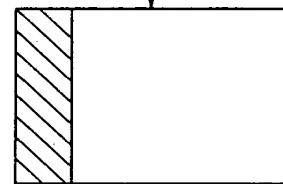
**LOGICAL STRUCTURE**



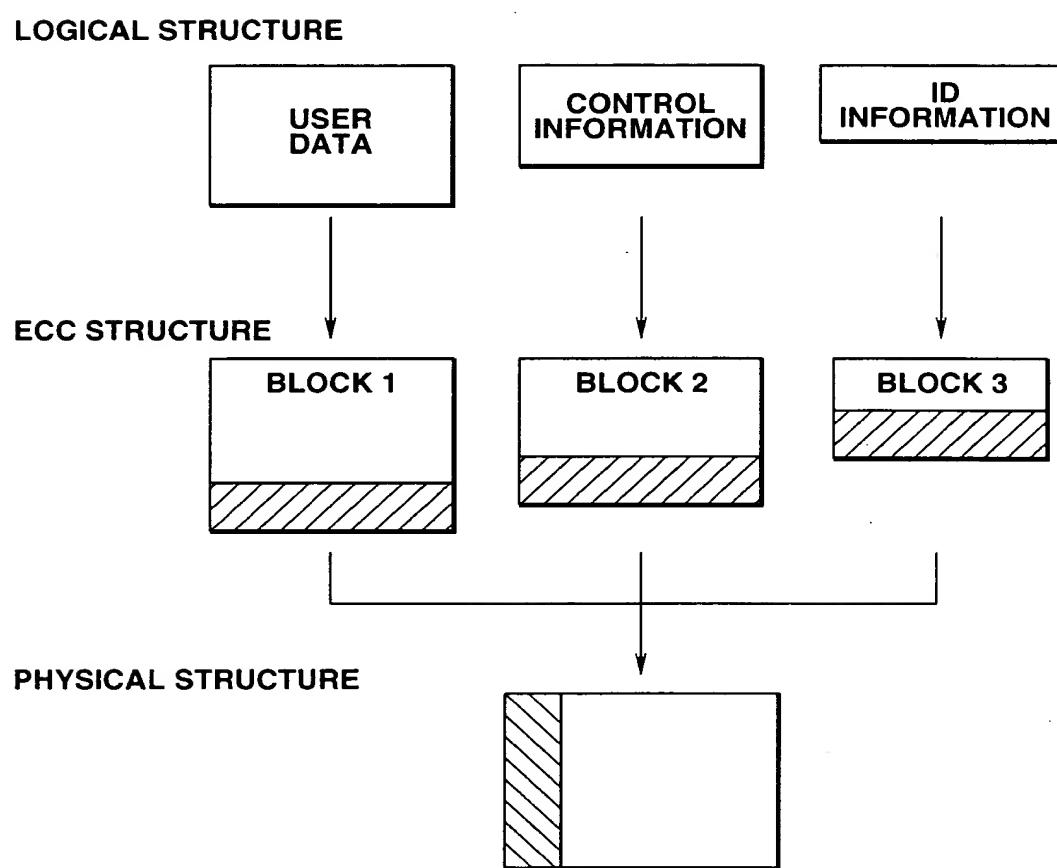
**ECC STRUCTURE**



**PHYSICAL STRUCTURE**

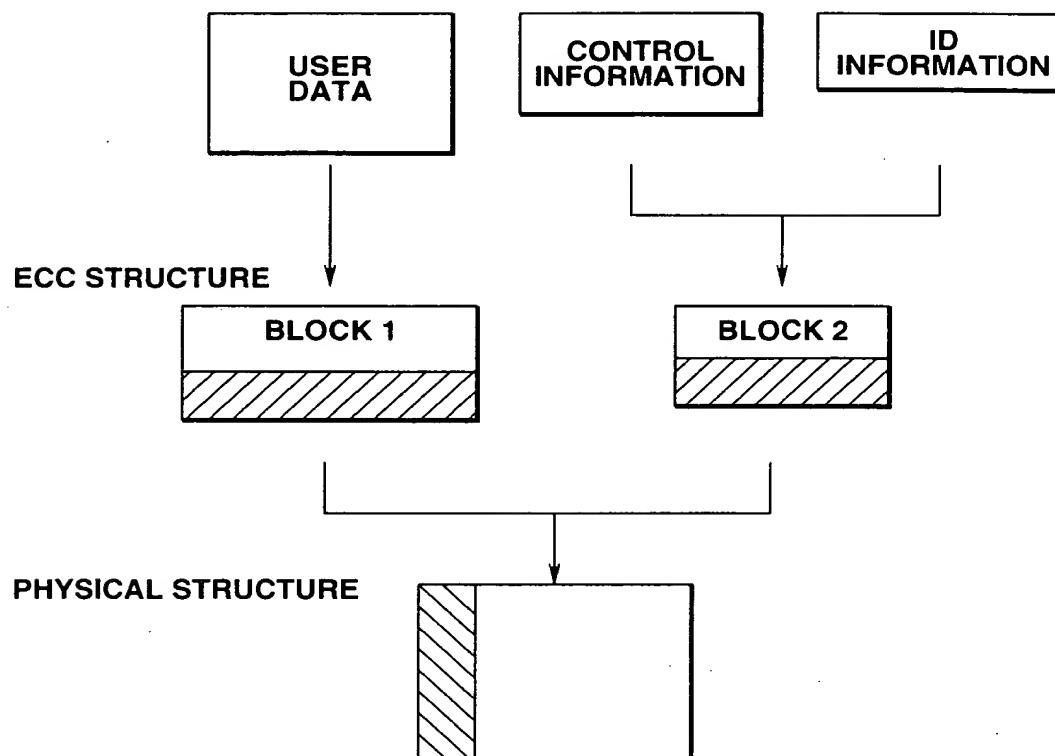


**FIG.6**

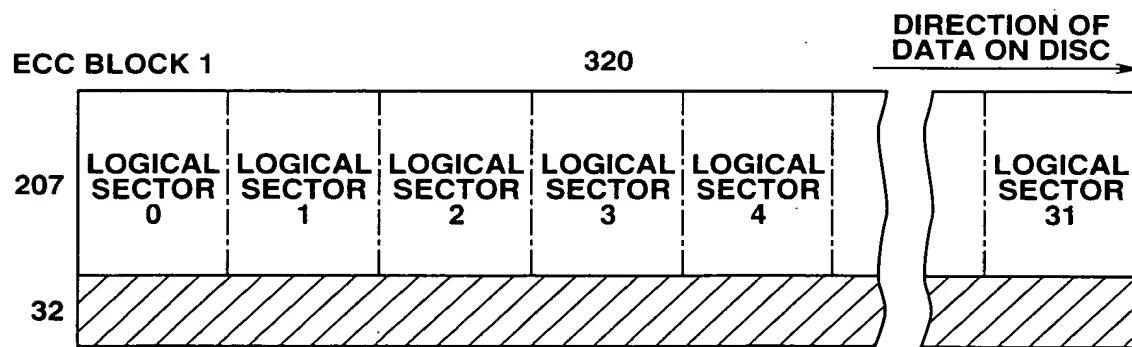


**FIG.7**

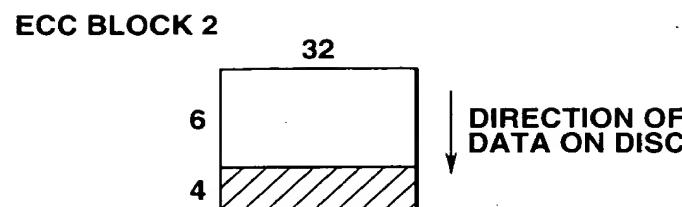
### LOGICAL STRUCTURE



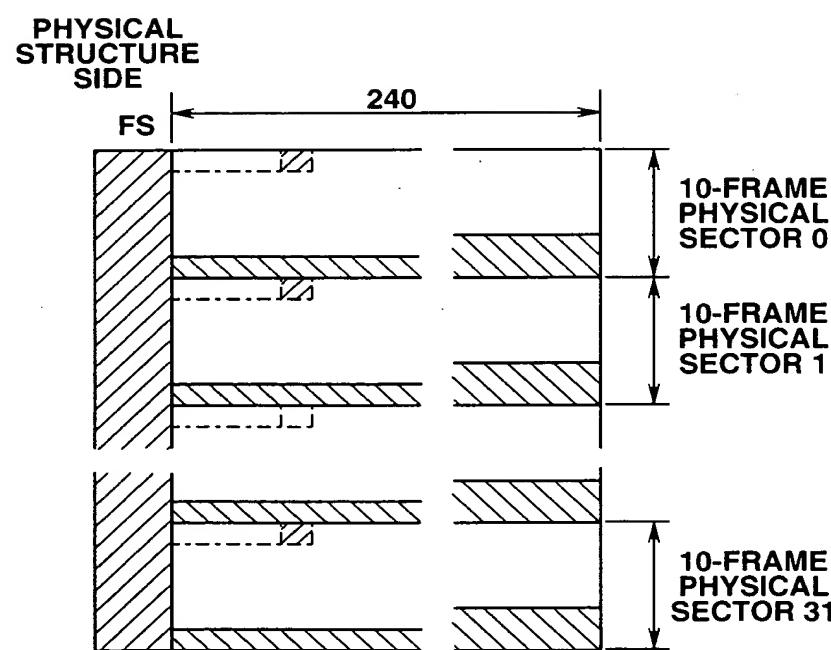
**FIG.8**



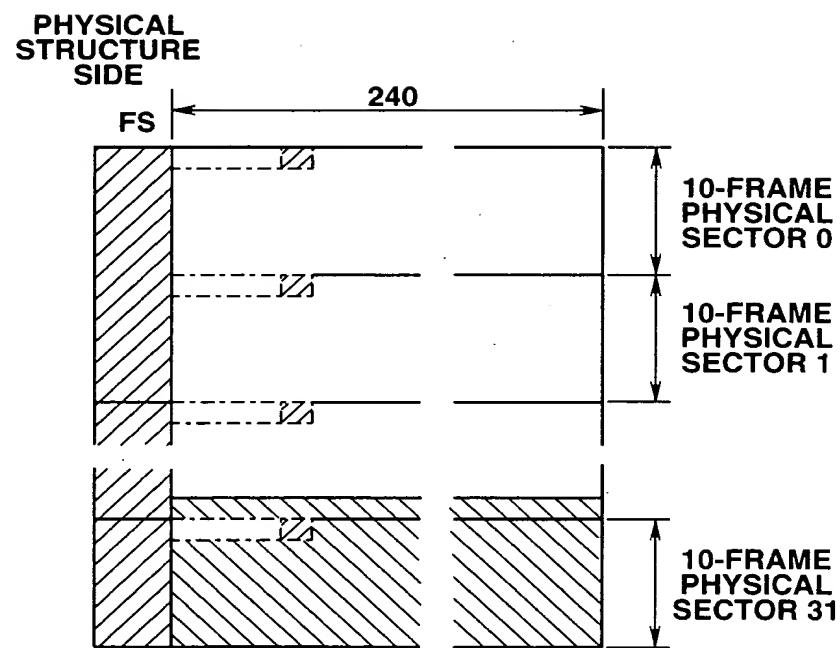
**FIG.9**



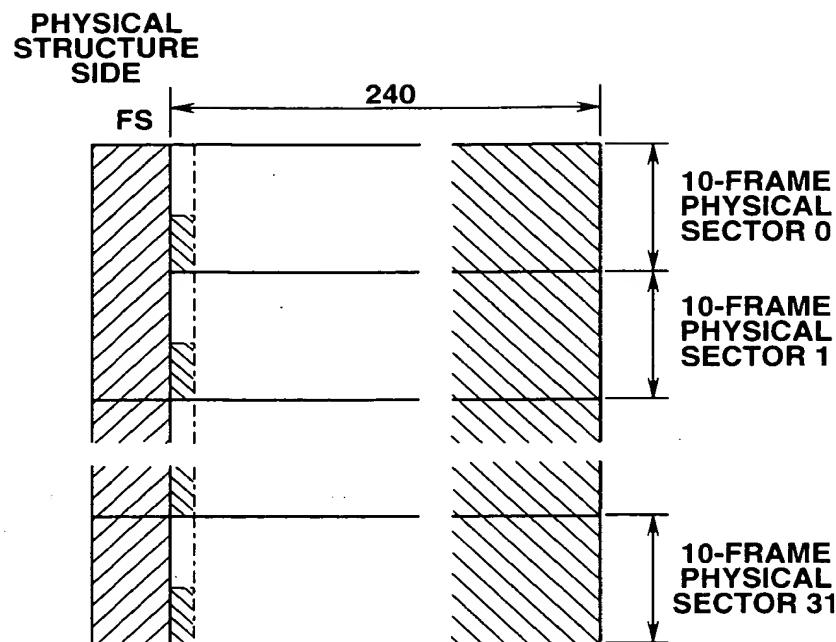
**FIG.10**



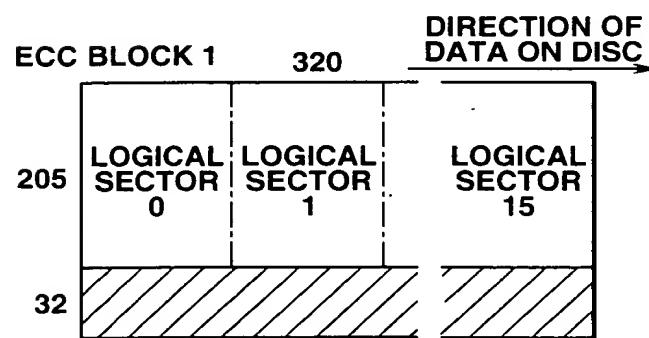
**FIG.11**



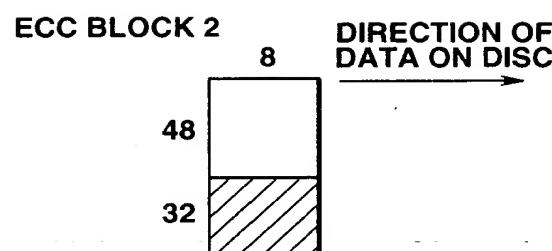
**FIG.12**



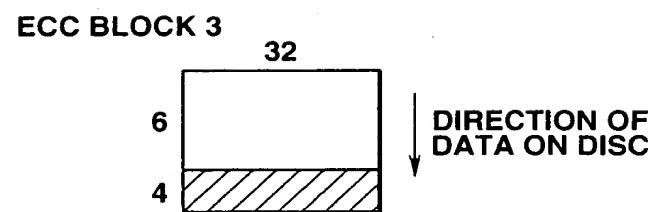
**FIG.13**



**FIG.14**

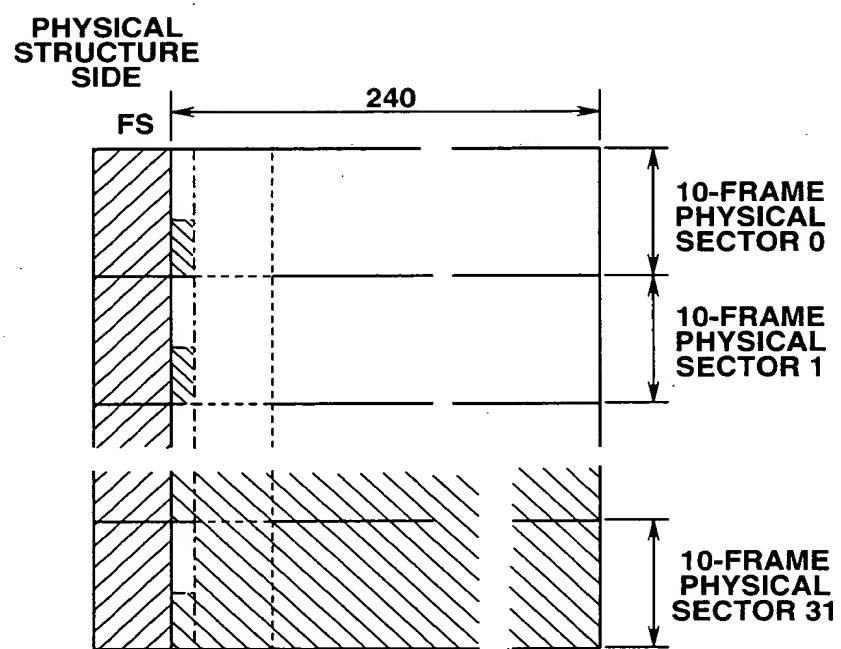


**FIG.15**

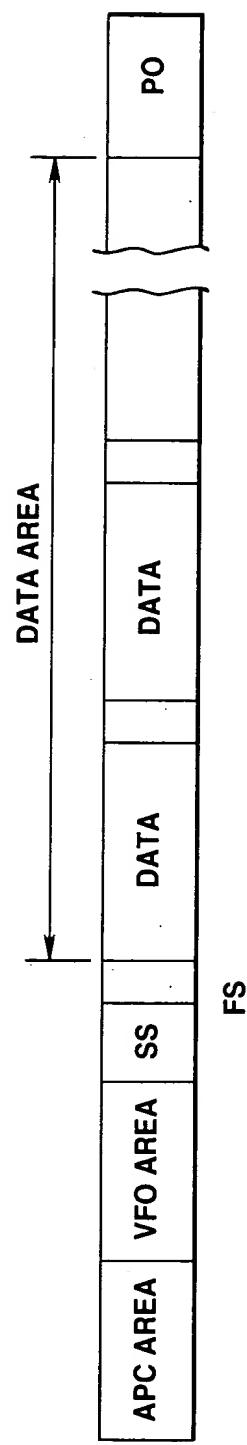


**FIG.16**

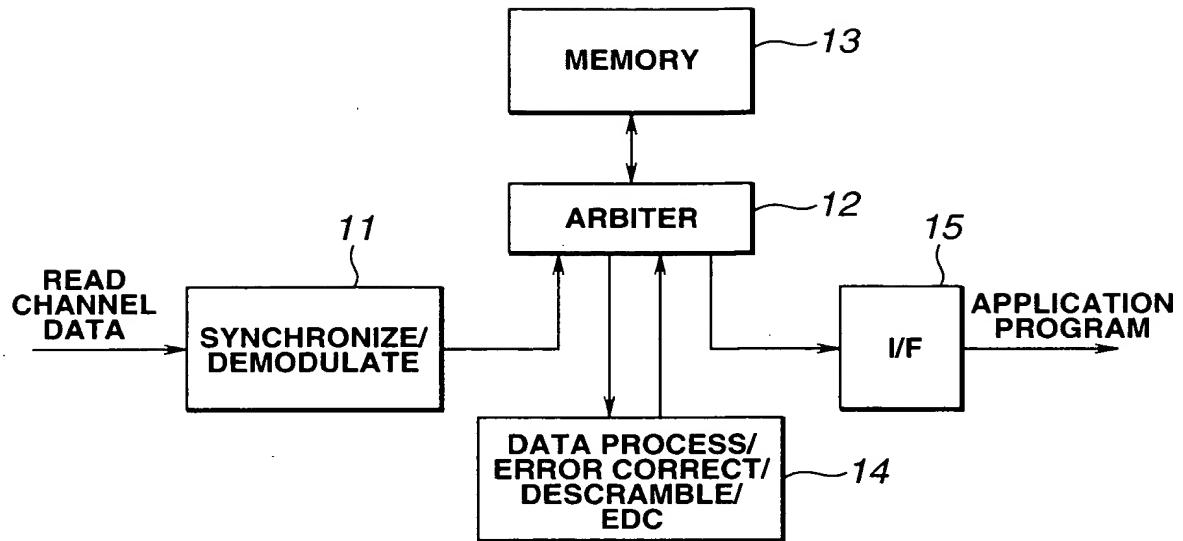
00510-00000000



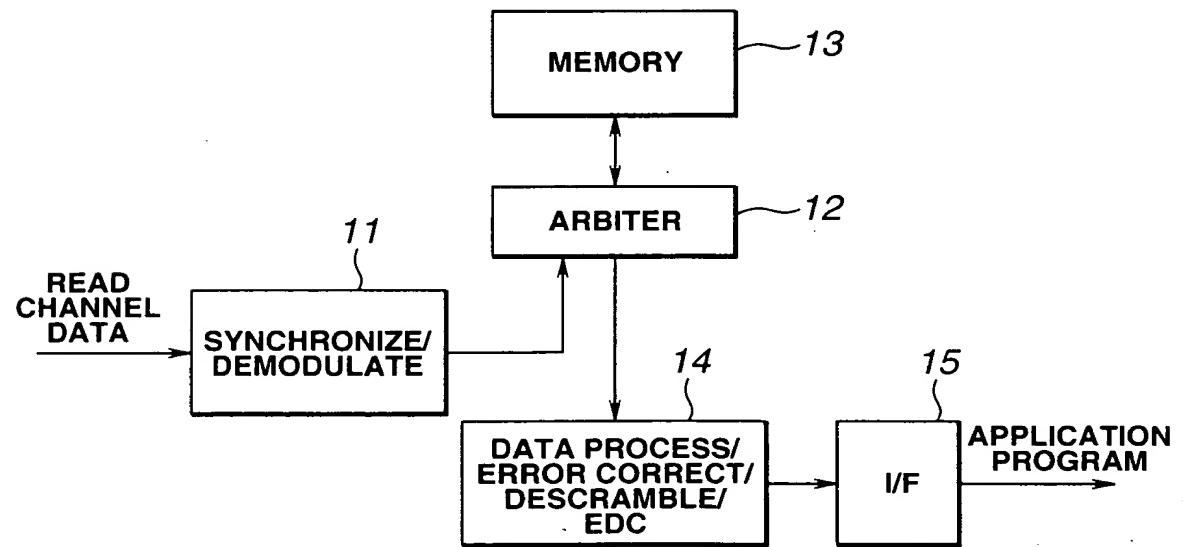
**FIG.17**

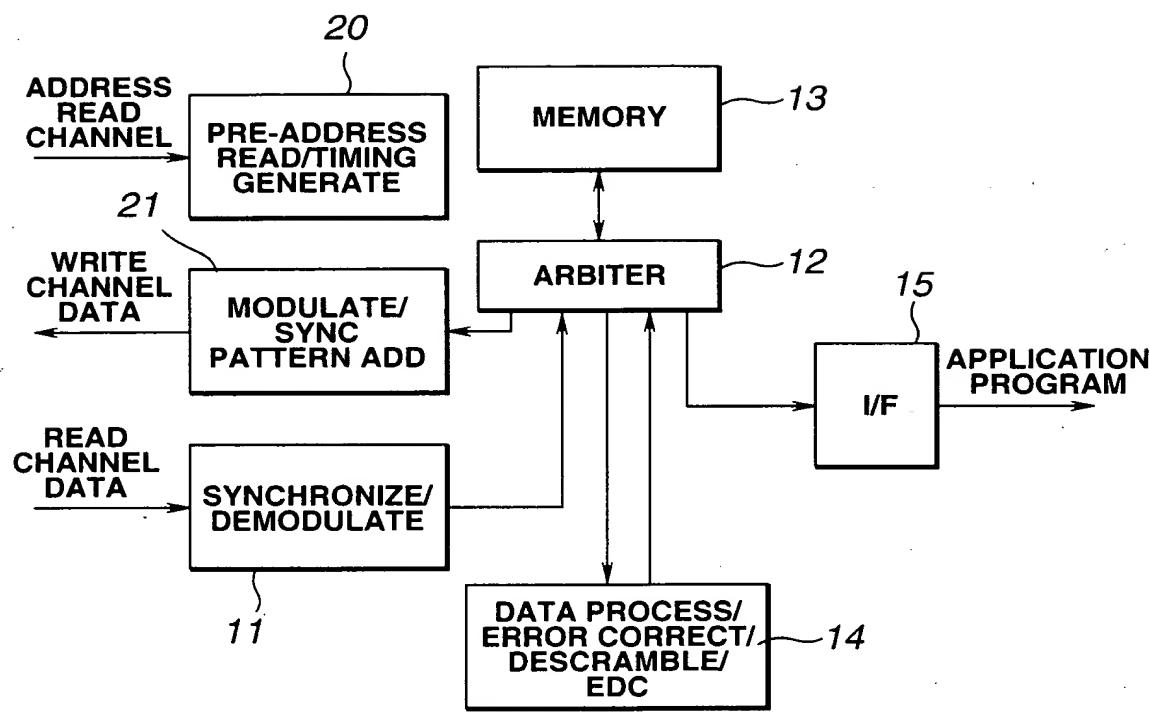


**FIG.18**



**FIG.19**





**FIG.21**